

Synthesis of Communication Schedules for TTEthernet-based Mixed-Criticality Systems

Domit̃ian T̃amaş-Selicean, Paul Pop, Wilfried Steiner

1. Introduction

- Safety-critical real-time applications, implemented using distributed architectures.
- Separation is required to implement applications of different criticality levels on the same architecture.

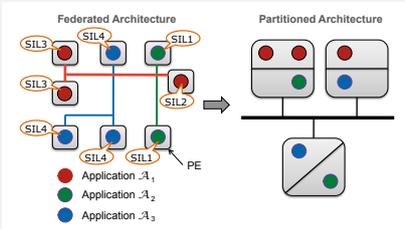


Figure 1. Mixed-criticality applications implemented using a federated architecture (left) and using a partitioned architecture (right).

2. TTEthernet is deterministic, synchronized and congestion-free, Ethernet based and ARINC 664p7 compliant.

- Separation achieved by using virtual links.
- Provides three traffic classes:
 - Time Triggered (TT), sent based on static schedule tables and have the highest priority;
 - Rate Constrained (RC), transmitted if there are no TT messages to be sent;
 - Best Effort (BE), with the lowest priority.

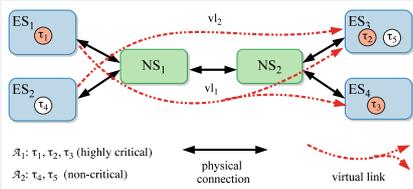


Figure 2. Example of a TTEthernet network, composed of 4 ESes, ES_1 to ES_4 , and 2 NSes, NS_1 and NS_2 . Each ES consists of a PE containing a CPU, RAM and non-volatile memory, and a network interface card (NIC).

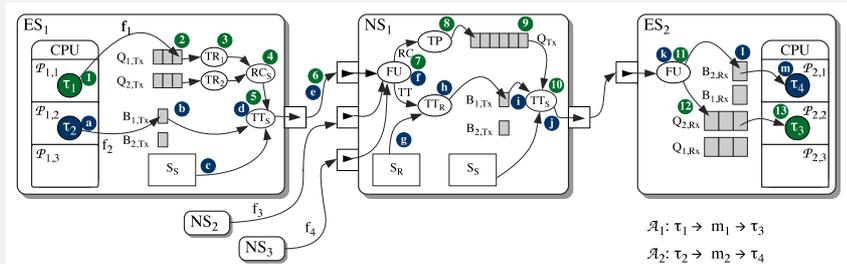


Figure 3. TT and RC message transmission example.

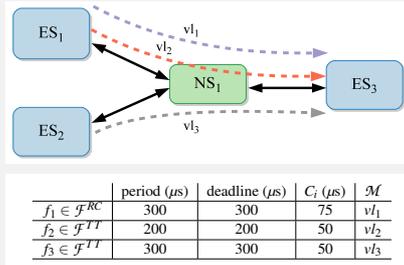


Figure 4. Example system model. Example architecture model (upper figure) and example application model (lower figure). \mathcal{F}^{TT} is the set of TT frames, while \mathcal{F}^{RC} is the set of RC frames.

3. Problem Formulation

- Find the set of TT schedules
- such that
 - the deadlines for the given TT and RC frames are satisfied and
 - the end-to-end delay of RC frames is minimized.

4. Schedule Optimization Tabu Search (TS) based optimization strategy.

- The cost function:

$$Cost = w_{TT} \times \delta_{TT} + w_{RC} \times \delta_{RC} \quad (1)$$

$$\delta_{TT/RC} = \begin{cases} c_1 = \sum_i \max(0, R_i - f_i.deadline) & \text{if } c_1 > 0 \\ c_2 = \sum_i (R_i - f_i.deadline) & \text{if } c_1 = 0 \end{cases} \quad (2)$$

- Moves applied to TT frames:
 - *advance*
 - *advance predecessors*
 - *postpone*
 - *postpone successors*
- The strategy reserves space for RC traffic using:
 - *add blank*
 - *remove blank*
 - *resize blank*

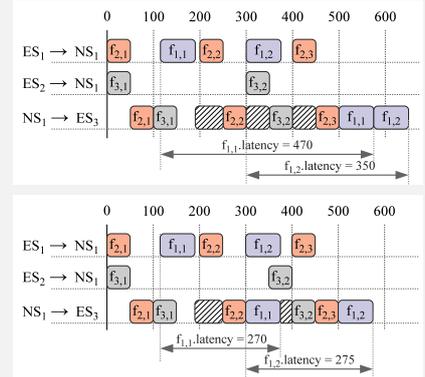


Figure 5. Worst case scenario for RC frame f_1 (see Fig. 2 for system details): Initial TT schedule (upper figure) and optimized TT schedule (lower figure). In this example the network implements the timely block integration algorithm, thus a RC frame is blocked from transmission on a link if a TT frame is scheduled to be sent before the RC frame would complete its transmission. The white boxes represent these "blocked" time intervals.

5. Experimental Evaluation

Set	Test case	ES	NS	Messages	Frame instances	Load [%]	Δ_{cost} [%]
1	11	13	4	80	12593	50	2.58
	12	25	6	88	1787	50	24.44
	13	35	8	103	2285	50	20.06
	14	45	10	165	3299	50	11.90
2	21	11	4	115	16904	70	9.17
	22	25	6	179	2523	70	20.61
	23	35	8	154	3698	70	39.34
	31	31	6	76	1387	40	37.97
3	32	33	25	88	1787	50	24.44
	33	33	25	115	2503	60	40.47
	34	34	25	179	2523	70	20.61
	35	35	25	155	2960	80	32.10
	41	41	35	65	1976	40	38.75
4	42	42	35	103	2285	50	20.06
	43	43	35	89	2801	60	12.73
	44	44	35	176	3856	70	12.75
	45	45	35	135	3490	80	20.23
	5	automotive	15	3	170	38305	80

Figure 6. Experimental evaluation results. The last column presents the comparison between the proposed strategy and the baseline solution, as a percentage improvement in terms of the cost function (see Eq. 1).

Contact information:

Domit̃ian T̃amaş-Selicean

Technical University of Denmark
Kongens Lyngby, Denmark
dota@imm.dtu.dk

Paul Pop

Technical University of Denmark
Kongens Lyngby, Denmark
paul.pop@imm.dtu.dk

Wilfried Steiner

TTTech Computertechnik AG
Vienna, Austria
wilfried.steiner@tttech.com