

Publications

Books:

- [B1] The RAISE Language Group. *The RAISE Specification Language*. Prentice Hall, 1992.
- [B2] The RAISE Method Group. *The RAISE Development Method*. Prentice Hall, 1995.
- [B3] Hubert Baumeister, Maura Cerioli, Anne Haxthausen, Till Mossakowski, Peter D. Mosses, Donald Sannella, and Andrzej Tarlecki. *CASL Reference Manual*, Part III Semantics. Number 2960 in IFIP LNCS series. Springer, 2004.
- [B4] Chris George and Anne E. Haxthausen. The Logic of the RAISE Specification Language. In *Logics of Specification Languages*, EATCS. Springer, 2008.
- [B5] Till Mossakowski, Anne Haxthausen, Donald Sannella, and Andrzej Tarlecki. CASL, the Common Algebraic Specification Language. In *Logics of Specification Languages*, EATCS. Springer, 2008.
- [B6] John S. Fitzgerald, Anne E. Haxthausen, and Husnu Yenigun, editors. *Theoretical Aspects of Computing – ICTAC 2008*. Number 5160 in Lecture Notes in Computer Science. Springer, 2008.

Papers in journals and proceedings of conferences:

- [A1] Anne E. Haxthausen. Mutually Recursive Algebraic Domain Equation. In *Proceedings of VDM'88*, volume 328 of *Lecture Notes in Computer Science*. Springer-Verlag, 1988.
- [A2] Anne E. Haxthausen. Recursive Algebraic Specifications and their Semantics. In *Proceedings of the IFIP'89*. North-Holland, 1989.
- [A3] Anne E. Haxthausen. Parameterized Algebraic Domain Equation. In *Proceedings of Categorical Methods in Computer Science*, volume 393 of *Lecture Notes in Computer Science*. Springer-Verlag, 1989.
- [A4] Dines Bjørner, Anne E. Haxthausen, and Klaus Havelund. Formal, Model-oriented Software Development Methods - From VDM to ProCoS & from RAISE to LaCoS. *Future Generation Computer Systems*, (7), 1991/92. Also in *Proceedings from InfoJapan'90*, 1990.
- [A5] Anne E. Haxthausen, Jan Storbank Pedersen, and Søren Prehn. RAISE: a Product Supporting Industrial Use of Formal Methods. *Technique et Science Informatiques*, 12(3):319–346, 1993.

- [A6] Anne E. Haxthausen and Chris W. George. A Concurrency Case Study Using RAISE. In *Proceedings of FME'93: Industrial Strength Formal Methods*, volume 670 of *Lecture Notes in Computer Science*. Springer-Verlag, 1993.
- [A7] Anne E. Haxthausen. Order-sorted Algebraic Specifications with Higher-order Functions. In *Proceedings of AMAST'95*, number 936 in *Lecture Notes in Computer Science*, pages 133–151. Springer-Verlag, 1995.
- [A8] Anne E. Haxthausen. Developing a Translator from C Programs to Data Flow Graphs Using RAISE. In *Proceedings of COMPASS'96*. IEEE Computer Society, 1996.
- [A9] Anne E. Haxthausen and Friederike Nickl. Pushouts of Order-sorted Algebraic Specifications. In *Proceedings of AMAST'96*, number 1101 in *Lecture Notes in Computer Science*. Springer-Verlag, 1996.
- [A10] Anne E. Haxthausen. Order-sorted Algebraic Specifications with Higher-order Functions. *Theoretical Computer Science*, 183:157–185, 1997.
- [A11] Jan Madsen, Jesper Grode, Peter V. Knudsen, Morten E. Petersen, and Anne Haxthausen. LYCOS: the Lyngby Co-Synthesis System. *Design Automation for Embedded Systems*, 2(2):195 – 236, March 1997.
- [A12] Maura Cerioli, Anne Haxthausen, Bernd Krieg-Brückner, and Till Mossakowski. Permissive Subsorted Partial Logic in CASL. In *Proceedings of AMAST'97*, number 1349 in *Lecture Notes in Computer Science*. Springer-Verlag, 1997.
- [A13] Anne Haxthausen and Jan Peleska. Formal Development and Verification of a Distributed Railway Control System. In *Proceedings of First FMERail Workshop*, 1998.
- [A14] Anne Haxthausen and Xia Yong. A RAISE Specification Framework and Justification Assistant for the Duration Calculus. In *Proceedings of ESSLLI-98 Workshop on Duration Calculus*, pages 51–58, 1998.
- [A15] Anne E. Haxthausen and Jan Peleska. Formal Development and Verification of a Distributed Railway Control System. In *Proceedings of Formal Methods World Congress FM'99*, number 1709 in *Lecture Notes in Computer Science*, pages 1546 – 1563. Springer-Verlag, 1999.
- [A16] Anne E. Haxthausen and Jan Peleska. Formal Development and Verification of a Distributed Railway Control System. *IEEE Transaction on Software Engineering*, 26(8):687–701, 2000.
- [A17] Till Mossakowski, Anne Haxthausen, and Bernd Krieg-Brückner. Subsorted Partial Higher-order Logic as an extension of CASL. In *Proceedings of WADT'99*, number 1827 in *Lecture Notes in Computer Science*, pages 126 – 145. Springer-Verlag, 2000.

- [A18] Anne Haxthausen and Xia Yong. Linking DC together with TRSL. In *Proceedings of 2nd International Conference on Integrated Formal Methods (IFM'2000), Schloss Dagstuhl, Germany, November 2000*, number 1945 in Lecture Notes in Computer Science, pages 25–44. Springer-Verlag, 2000.
- [A19] Anne Haxthausen. Some Approaches for Integration of Specification Techniques. In *Proceedings of INT'00 – Integration of Specification Techniques with Applications in Engineering*, pages 33 – 40. Technical Report 2000/04, Technical University of Berlin, 2000. (Invited extended abstract).
- [A20] Jan Peleska, Alexander Baer, and Anne E. Haxthausen. Towards Domain-Specific Formal Specification Languages for Railway Control Systems. In *Proceedings of the 9th IFAC Symposium on Control in Transportation Systems 2000, June 13-15, 2000, Braunschweig, Germany*, pages 147–152, 2000.
- [A21] Morten P. Lindegaard, Peter Viuf, and Anne E. Haxthausen. Modelling Railway Interlocking Systems. In *Proceedings of the 9th IFAC Symposium on Control in Transportation Systems 2000, June 13-15, 2000, Braunschweig, Germany*, pages 211–217, 2000.
- [A22] Anne E. Haxthausen and Jan Peleska. Formal Methods for the Specification and Verification of Distributed Railway Control Systems: From Algebraic Specifications to Distributed Hybrid Real-Time Systems. In *Forms '99 - Formale Techniken für die Eisenbahnsicherung Fortschritt-Berichte VDI, Reihe 12, Nr. 436*, pages 263–271. VDI-Verlag, Düsseldorf, 2000.
- [A23] Anne E. Haxthausen and Jan Peleska. A Domain Specific Language for Railway Control Systems. In *Proceedings of the Sixth Biennial World Conference on Integrated Design and Process Technology, (IDPT2002), Pasadena, California, June 23-28 2002*.
- [A24] Anne E. Haxthausen and Jan Peleska. Generation of Executable Railway Control Components from Domain-Specific Descriptions. In *Proceedings of the Symposium on Formal Methods for Railway Operation and Control Systems (FORMS'2003)*, pages 83–90. L'Harmattan Hongrie, 2003.
- [A25] Anne E. Haxthausen and Jan Peleska. Automatic Verification, Validation and Test for Railway Control Systems based on Domain-Specific Descriptions. In *Proceedings of the 10th IFAC Symposium on Control in Transportation Systems*. Elsevier Science Ltd, Oxford, 2003. ISBN 0-08-044059-2.
- [A26] Torben Gjaldbæk and Anne E. Haxthausen. Modelling and Verification of Interlocking Systems for Railway Lines. In *Proceedings of the 10th IFAC Symposium on Control in Transportation Systems*. Elsevier Science Ltd, Oxford, 2003. ISBN 0-08-044059-2.

- [A27] Chris George and Anne E. Haxthausen. The Logic of the RAISE Specification Language. *Computing and Informatics*, 22(3–4):323 – 350, 2003.
- [A28] Till Mossakowski, Anne Haxthausen, Donald Sannella, and Andrzej Tarlecki. CASL, the Common Algebraic Specification Language: Semantics and proof theory. *Computing and Informatics*, 22(3–4):285–322, 2003.
- [A29] Dines Bjørner, Chris W. George, Anne E. Haxthausen, Christian Krogh Madsen, Steffen Holmslykke, and Martin Penicka. "UML-ising" Formal Techniques. In *Proceedings of INT'2004 – Integration of Software Specification Techniques for Applications in Engineering*, number 3147 in Lecture Notes in Computer Science, pages 423 – 450. Springer, 2004. Invited paper.
- [A30] Morten P. Lindegaard and Anne E. Haxthausen. Proof Support for RAISE – by a Reuse Approach based on Institutions. In *Proceedings of AMAST'04*, number 3116 in Lecture Notes in Computer Science, pages 319–333. Springer-Verlag, 2004.
- [A31] Jan Peleska, Daniel Große, Anne E. Haxthausen, and Rolf Drechsler. Automated Verification for Train Control Systems. In *Proceedings of Formal Methods for Automation and Safety in Railway and Automotive Systems (FORMS/FORMAT 2004)*, Braunschweig, Germany, 2004.
- [A32] Anne E. Haxthausen, Nikolaj Christensen, and Rasmus Dyhrberg. From Domain Model to Domain-specific Language for Railway Control Systems. In *Proceedings of Formal Methods for Automation and Safety in Railway and Automotive Systems (FORMS/FORMAT 2004)*, Braunschweig, Germany, 2004.
- [A33] Chris W. George and Anne E. Haxthausen. Specification and Proof of the Mondex Electronic Purse. In *Proceedings of Automated Formal Methods 2006 (AFM 2006)*, Seattle, 2006.
- [A34] Anne E. Haxthausen, Chris W. George, and Marko Schütz. Specification and Proof of the Mondex Electronic Purse. In *Proceedings of 1st Asian Working Conference on Verified Software (AWCVS 2006)*, Macao. UNU-IIST, Report No. 347, 2006.
- [A35] Jan Peleska and Anne E. Haxthausen. Object Code Verification for Safety-Critical Railway Control Systems. In *Proceedings of Formal Methods for Automation and Safety in Railway and Automotive Systems (FORMS/FORMAT 2007)*, Braunschweig, Germany. GZVB e.V., 2007. ISBN 13:978-3-937655-09-3.
- [A36] Anne E. Haxthausen and Jan Peleska. A Domain-oriented, Model-based Approach for Construction and Verification of Railway Control Systems. In

Cliff B. Jones, Zhiming Liu, and Jim Woodcock, editors, *Formal Methods and Hybrid Real-Time Systems : Essays in Honour of Dines Bjørner and Zhou Chaochen on Occasion of their 70th Birthdays*, number 4700 in Lecture Notes in Computer Science, pages 320–348. Springer, 2007. Invited paper.

- [A37] Chris George and Anne E. Haxthausen. Specification, proof, and model checking of the Mondex electronic purse using RAISE. *Formal Aspects of Computing*, 20(1):101–116, 2008. Special issue on the Mondex challenge.
- [A38] Anne E. Haxthausen, Marie Le Bliguet, and Andreas A. Kjær. Modelling and Verification of Relay Interlocking Systems. In *15th Monterey Workshop: Foundations of Computer Software, Future Trends and Techniques for Development*, pages 47–55, 2008. Invited paper.
- [A39] Anne E. Haxthausen. Developing a Domain Model for Relay Circuits. *International Journal of Software and Informatics*, 3(2–3):241–272, 2009.
- [A40] Anne E. Haxthausen. A Domain-specific Framework for Automated Construction and Verification of Railway Control Systems. In B. Buth, G. Rabe, and T. Seyfarth, editors, *Proceedings of 28th International Conference on Computer Safety, Reliability, and Security, SAFECOMP 2009*, number 5775 in Lecture Notes in Computer Science, pages 1–3. Springer, 2009. Invited paper.
- [A41] Anne E. Haxthausen, Marie Le Bliguet, and Andreas A. Kjær. Modelling and Verification of Relay Interlocking Systems. In Christine Choppy and Oleg Sokolsky, editors, *15th Monterey Workshop: Foundations of Computer Software, Future Trends and Techniques for Development*, number 6028 in Lecture Notes in Computer Science. Springer, 2010. Invited paper.
- [A42] Anne E. Haxthausen. Towards a Framework for Modelling and Verification of Relay Interlocking Systems. In *16th Monterey Workshop: Modelling, Development and Verification of Adaptive Systems: the Grand Challenge for Robust Software*, 2010. Invited paper.
- [A43] Anne E. Haxthausen, Jan Peleska, and Sebastian Kinder. A Formal Approach for the Construction and Verification of Railway Control Systems. *Formal Aspects of Computing*, 23(2):191–219, 2011. Special issue in Honour of Dines Bjørner and Zhou Chaochen on Occasion of their 70th Birthdays. The article is also available electronically on Springer-Link: <http://www.springerlink.com/openurl.asp?genre=article&id=doi:10.1007/s00165-009-0143-6>.
- [A44] Anne E. Haxthausen. Towards a Framework for Modelling and Verification of Relay Interlocking Systems. In *16th Monterey Workshop: Modelling, Development and Verification of Adaptive Systems: the Grand Challenge for*

Robust Software, number 6662 in Lecture Notes in Computer Science, pages 176–192. Springer, 2011. Invited paper. Extended version of [A42].

- [A45] Anne E. Haxthausen, Andreas A. Kjær, and Marie Le Bliguet. Formal Development of a Tool for Automated Modelling and Verification of Relay Interlocking Systems. In *17th International Symposium on Formal Methods (FM 2011)*, number 6664 in Lecture Notes in Computer Science, pages 118–132. Springer, 2011.
- [A46] Jan Peleska, Johannes Feuser, and Anne E. Haxthausen. The Model-Driven openETCS Paradigm for Secure, Safe and Certifiable Train Control Systems. In *Railway Safety, Reliability and Security: Technologies and System Engineering*, pages 22–52. IGI Global, 2012.
- [A47] Anne E. Haxthausen. Automated Generation of Safety Requirements from Railway Interlocking Tables. In *5th International Symposium On Leveraging Applications of Formal Methods, Verification and Validation (ISOLA'2012), Part II*, number 7610 in Lecture Notes in Computer Science, pages 261–275. Springer, 2012.
- [A48] Ekkart Kindler, Hubert Baumeister, Anne Elisabeth Haxthausen, and Joseph Kiniry. On the dimensions of software documents an idea for framing the software engineering process. In I. Jacobsen, M. Goedicke, and P. Johnson, editors, *The Semat Workshop on a General Theory of Software Engineering 2012*, pages 21–22. Springer, 2012.
- [A49] Anne E. Haxthausen and Jan Peleska. Efficient Development and Verification of Safe Railway Control Software. In *Railways: Types, Design and Safety Issues*, pages 127–148. Nova Science Publishers, Inc., 2013.
- [A50] Marieta V. Fasie, Anne E. Haxthausen, and Joseph R. Kiniry. A Rigorous Methodology for Analyzing and Designing Plug-Ins. In *Proceedings of the 3rd International Workshop on Developing Tools as Plug-ins (TOPI 2013)*, pages 49–50. IEEE, 2013.
- [A51] Linh Vu Hong, Anne Elisabeth Haxthausen, and Jan Peleska. Formal Development and Verification of Railway Control Systems, 2013. Presented at Conference of Strategic Research in Transportation and Infrastructure, Technical University of Denmark.
- [A52] Nick Battle, Anne Haxthausen, Sako Hiroshi, Peter Jørgensen, Nico Plat, Shin Sahara, and Marcel Verhoef. The Overture Approach to VDM Language Evolution. In Ken Pierce and Stefan Hallerstede, editors, *Proceedings of the 11th Overture Workshop*, number Technical Report ECE-TR-170, pages 8–15. 2013.

- [A53] Anne E. Haxthausen, Jan Peleska, and Ralf Pinger. Applied Bounded Model Checking for Interlocking System Designs. In Stefan Gruner, Anne E. Haxthausen, Tom Maibaum, and Markus Roggenbach, editors, *Towards a Formal Methods Body of Knowledge for Railway Control and Safety Systems. Proceedings of FM-RAIL-BOK Workshop 2013.*, number DTU Compute Technical Report-2013-20 (ISBN 978-87-643-1303-1), pages 21–26. 2013.
- [A54] Anne E. Haxthausen, Jan Peleska, and Ralf Pinger. Applied Bounded Model Checking for Interlocking System Designs. In Steve Counsell and Manuel Núñez, editors, *Software Engineering and Formal Methods*, volume 8368 of *Lecture Notes in Computer Science*, pages 205–220. Springer, 2014.
- [A55] Anne E. Haxthausen. Automated Generation of Formal Safety Conditions from Railway Interlocking Tables. *International Journal on Software Tools for Technology Transfer (STTT), Special Issue on Formal Methods for Railway Control Systems*, 16(6):713–726, 2014.
- [A56] Anne E. Haxthausen. An Institution for Imperative RSL Specifications. In Shusaku Iida, José Meseguer, and Kazuhiro Ogata, editors, *Specification, Algebra, and Software. Essays Dedicated to Kokichi Futatsugi*, number 8373 in *Lecture Notes in Computer Science*, pages 441–464. Springer, 2014.
- [A57] Linh H. Vu, Anne E. Haxthausen, and Jan Peleska. Formal Verification of the Danish Railway Interlocking Systems. In Marieke Huisman and Jaco van de Pol, editors, *Pre-proceedings of 14th International Workshop on Automated Verification of Critical Systems (AVoCS 2014)*, CTIT Workshop Proceedings Series WP 14-01, pages 257–258. University of Twente, 2014.
- [A58] Linh H. Vu, Anne E. Haxthausen, and Jan Peleska. A Domain-Specific Language for Railway Interlocking Systems. In Eckehard Schnieder and Géza Tarnai, editors, *FORMS/FORMAT 2014 - 10th Symposium on Formal Methods for Automation and Safety in Railway and Automotive Systems*, pages 200–209. Institute for Traffic Safety and Automation Engineering, Technische Universität Braunschweig, 2014. Got best-paper-award.
- [A59] Linh H. Vu, Anne E. Haxthausen, and Jan Peleska. Formal Modeling and Verification of Interlocking Systems Featuring Sequential Release. In *Third International Workshop on Formal Techniques for Safety-Critical Systems (FTSCS 2014) Preliminary Proceedings*, pages 58–73, 2014. For a revised post proceedings version, see [A61].
- [A60] Cécile Braunstein, Anne E Haxthausen, Wen-ling Huang, Felix Hübner, Jan Peleska, Uwe Schulze, and Linh Vu Hong. Complete Model-Based Equivalence Class Testing for the ETCS Ceiling Speed Monitor. In *Formal Methods*

and Software Engineering, pages 380–395. Springer International Publishing, 2014.

- [A61] Linh H. Vu, Anne E. Haxthausen, and Jan Peleska. Formal Modeling and Verification of Interlocking Systems Featuring Sequential Release. In *Formal Techniques for Safety-Critical Systems*, volume 476 of *Communications in Computer and Information Science*, pages 223–238. Springer International Publishing Switzerland, 2015. Revised version of [A59].
- [A62] Anne E. Haxthausen and Jan Peleska. Model-checking and Model-based Testing in the Railway Domain. In *Formal Modeling and Verification of Cyber-Physical Systems*, Lecture Notes in Computer Science, pages 82–121. Springer, 2015.
- [A63] Anne E. Haxthausen, Hoang Nga Nguyen, and Markus Roggenbach. Comparing Formal Verification Approaches of Interlocking Systems. In Thierry Lecomte, Ralf Pinger, and Alexander Romanovsky, editors, *Reliability, Safety, and Security of Railway Systems. Modelling, Analysis, Verification, and Certification: First International Conference, RSSRail Proceedings*, pages 160–177. Springer International Publishing, 2016.
- [A64] Alessandro Fantechi, Stefania Gnesi, Anne Haxthausen, Jaco van de Pol, Marco Roveri, and Helen Treharne. SaRDIn - A Safe Reconfigurable Distributed Interlocking. In *Proceedings of 11th World Congress on Railway Research (WCRR 2016)*, 2016.
- [A65] Anne E. Haxthausen and Peter H. Østergaard. On the Use of Static Checking in the Verification of Interlocking Systems. In *Leveraging Applications of Formal Methods, Verification and Validation: Discussion, Dissemination, Applications, Part II*, volume 9953 of *Lecture Notes in Computer Science*, pages 266–278. Springer International Publishing AG, 2016.
- [A66] Hugo D. Macedo, Alessandro Fantechi, and Anne E. Haxthausen. Compositional Verification of Multi-Station Interlocking Systems. In *Leveraging Applications of Formal Methods, Verification and Validation: Discussion, Dissemination, Applications, Part II*, volume 9953 of *Lecture Notes in Computer Science*, pages 279–293. Springer International Publishing AG, 2016.
- [A67] Anne E. Haxthausen and Jan Peleska. On the Feasibility of a Unified Modelling and Programming Paradigm. In *Leveraging Applications of Formal Methods, Verification and Validation*, Lecture Notes in Computer Science. In *Leveraging Applications of Formal Methods, Verification and Validation: Discussion, Dissemination, Applications, Part II*, volume 9953 of *Lecture Notes in Computer Science*, pages 32–49. Springer, 2016.

- [A68] Linh Hong Vu, Anne E. Haxthausen, and Jan Peleska. Formal modelling and verification of interlocking systems featuring sequential release. *Science of Computer Programming*, 133, Part 2:91 – 115, 2017. <http://dx.doi.org/10.1016/j.scico.2016.05.010>.
- [A69] Alessandro Fantechi, Anne E. Haxthausen, and Michel B. R. Nielsen. Model checking geographically distributed interlocking systems using UMC. In *2017 25th Euromicro International Conference on Parallel, Distributed and Network-based Processing (PDP)*, pages 278–286, 2017.
- [A70] Hugo Daniel Macedo, Alessandro Fantechi, and Anne E. Haxthausen. Compositional model checking of interlocking systems for lines with multiple stations. In Clark Barrett, Misty Davies, and Temesghen Kahsai, editors, *NASA Formal Methods: 9th International Symposium, NFM 2017, Proceedings*, volume 10227 of *Lecture Notes in Computer Science*, pages 146–162. Springer International Publishing, 2017.
- [A71] Alessandro Fantechi, Anne E. Haxthausen, and Hugo Daniel Macedo. Compositional verification of interlocking systems for large stations. In Alessandro Cimatti and Marjan Sirjani, editors, *International Conference on Software Engineering and Formal Methods*, volume 10469 of *Lecture Notes in Computer Science*, pages 236–252. Springer, 2017.
- [A72] Linh H. Vu, Anne E. Haxthausen, and Jan Peleska. A domain-specific language for generic interlocking models and their properties. In Alessandro Fantechi, Thierry Lecomte, and Alexander Romanovsky, editors, *Reliability, Safety, and Security of Railway Systems. Modelling, Analysis, Verification, and Certification: Second International Conference, RSSRail 2017, Pistoia, Italy, November 14-16, 2017, Proceedings*, pages 99–115, Cham, 2017. Springer International Publishing.
- [A73] Signe Geisler and Anne E. Haxthausen. Stepwise Development and Model Checking of a Distributed Interlocking System - Using RAISE. In Klaus Havelund, Jan Peleska, Bill Roscoe, and Erik de Vink, editors, *Formal Methods*, *Lecture Notes in Computer Science*, pages 277–293. Springer International Publishing, 2018.
- [A74] Alessandro Fantechi and Anne E. Haxthausen. Safety interlocking as a distributed mutual exclusion problem. In Falk Howar and Jiří Barnat, editors, *Formal Methods for Industrial Critical Systems*, pages 52–66. Springer International Publishing, 2018.
- [A75] Jan Peleska, Niklas Krafczyk, Anne E. Haxthausen, and Ralf Pinger. Efficient Data Validation for Geographical Interlocking Systems. To appear in *RSSRail Proceedings*, 2019.

Selected, recent reports:

- [R1] Stefan Gruner, Anne E. Haxthausen, Tom Maibaum, and Markus Roggenbach (editors). Towards a Formal Methods Body of Knowledge for Railway Control and Safety Systems. Technical Report DTU Compute Technical Report-2013-20, Technical University of Denmark, November 2013. ISBN 978-87-643-1303-1.
- [R2] Cécile Braunstein, Jan Peleska, Uwe Schulze, Felix Hübner, Wen-Ling Huang, Anne E. Haxthausen, and Linh Vu Hong. A SysML Test Model and Test Suite for the ETCS Ceiling Speed Monitor. Technical Report OETCS/WP4/CSM-01/00, openETCS, May 2014.